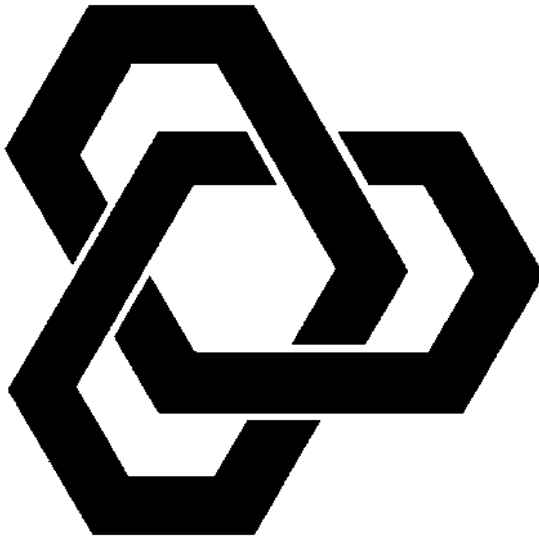


TAMS 62622 PCI GPIO Card



Installation & Operation Instructions

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Installation & Operating Instructions

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General Information

Overview of the TAMS 62622 GPIO Interface

This guide explains how to install, configure, and utilize enhancements of the TAMS 62622 GPIO (General Purpose Input Output) interface.

GPIO is a parallel interface that is flexible and allows a variety of custom connections. A PCI expansion slot is required to accommodate the GPIO interface card.

The TAMS 62622 card has a rotary switch on the upper edge that is used in systems running Windows 2000 / XP.

This guide also provides an explanation of the configuration process as it relates to the HP/Agilent Standard Instrument Control Library (SICL). A detailed description of the TAMS 62622's functionality is included to aid in this configuration process.

The TAMS 62622 provides 16 bit data exchange with peripheral devices that do not support more common interface protocols like HP-IB or RS-232. Connection flexibility is augmented in the TAMS 62622 GPIO interface by extra status and control lines, a choice of handshake methods, several data-latching options, and selectable data width and polarity.

There are two basic modes for the data IO in the TAMS 62622 GPIO interface: *Compatibility Mode* and *Enhanced Mode*. The TAMS 62622 is by default configured in *Compatibility Mode* which is supported by all HP/TAMS GPIO cards. Alternatively, the TAMS 62622 can be configured with a bi-directional data port and auxiliary control lines (*Enhanced Mode*) which is supported by HP 2074/5 and all TAMS GPIO cards.

Installing the GPIO Interface

This section explains how to install the TAMS 62622 GPIO interface in the computer. To complete the installation:

1. Make sure the computer power switch is off.
2. Refer to the Owner's Guide of your computer for instructions on opening your computer and installing PCI boards.
3. If you are installing more than one 62622 GPIO card in your PC, each card must be at a unique address as determined by the rotary switch on the card. A single 62622 card can have any given rotary switch setting, multiple cards must all have unique settings.
4. Install the GPIO interface in the PC by plugging the card into the PCI slot. Make sure that the card is firmly seated in the slot.
5. Follow the instructions being careful to handle the TAMS 62622 board only by its

metal bracket. Avoid contact with the edge connector. After the board has been plugged in and the retaining screw installed the computer should be reassembled.

6. Prepare and install the GPIO interface cable.

Note: The TAMS 62622 is a PCI device. Unlike most EISA and ISA devices a PCI device does not require an I/O address or IRQ setting. These settings are handled automatically.

Related Software Documentation

Standard Instrument Control Library for Windows

To configure the TAMS 62622 GPIO interface for the HP/Agilent Standard Instrument Control Library (HP/Agilent SICL) for Windows, see the “Installing and Configuring the HP/Agilent I/O Libraries” chapter of the *HP/Agilent I/O Libraries Installation and Configuration Guide for Windows*.

To develop HP/Agilent SICL I/O applications for the TAMS 62622 on a PC, see the “Using GPIO with HP/Agilent SICL” chapter of the *HP/Agilent SICL User’s Guide for Windows*. HP/Agilent SICL functions, including those that are GPIO specific, are fully described in the HP/Agilent SICL Reference Manual.

tamsgpio Utility

The tamsgpio program allows one to configure and issue reads/writes to the GPIO bus via the 62622 card. The manual for this program can be found by clicking through the Start menu at: Programs -> TAMS GPIO -> Documentation -> Tamsgpio manual.

Driver And Software Installation

Driver installation assumes basic knowledge about software installation procedures specific to the platform. Refer to your platform specific operating system documentation or contact your system administrator.

To install the t62622 driver and associated software, it is not necessary for the TAMS 62622 interface card(s) to be present in the system. Configuration, however, requires the card(s) and driver to both be present along with the following referenced packages.

Important! The HP/Agilent IO libraries (SICL) (Version M.01.00 or later) or I/O Libraries Suite 14.1 with Patch 2 must be installed prior to proceeding with the driver installation for the TAMS 62622. Revisions of I/O Libraries subsequent to 14.1 with Patch 2 should also be acceptable. The TAMS 62622 installer will detect if SICL is not installed and abort the installation. If the Agilent IO libraries are reinstalled it will be necessary to reinstall the 62622 driver.

Important! The Microsoft .NET Framework ,version 1.1 or later must be installed prior to proceeding with the driver installation for the 62622. The TAMS 62622 installer will automatically detect if .NET Framework is not installed and abort the installation. The TAMS 62622 CD contains the install packages for Microsoft .NET framework 1.1. To install .NET Framework 1.1 along with Microsoft recommended updates, simply execute the script (install_dotnet1.1.bat) on the TAMS 62622 CD. Make sure and wait for all three install packages run by this script to complete prior to rebooting.

The TAMS 62622 card and software is NOT a Plug-N-Play device. If the 62622 card is installed and your computer is rebooted prior to installing the 62622 driver, the *Found New Hardware Wizard* will detect the newly installed 62622 card. At this point it is best to CANCEL this Wizard (if and only if you have not yet installed the TAMS 62622 driver). Rather use the following method to install the 62622 driver.

1. Check to make sure that the prerequisites as noted above are installed by looking in the *Add or Remove Programs* window under the *Control Panel*.

If they are not listed in the *Add or Remove Programs* windows install these packages. Alternatively, do step 2 and if the prerequisites are not installed step 2 will automatically abort.

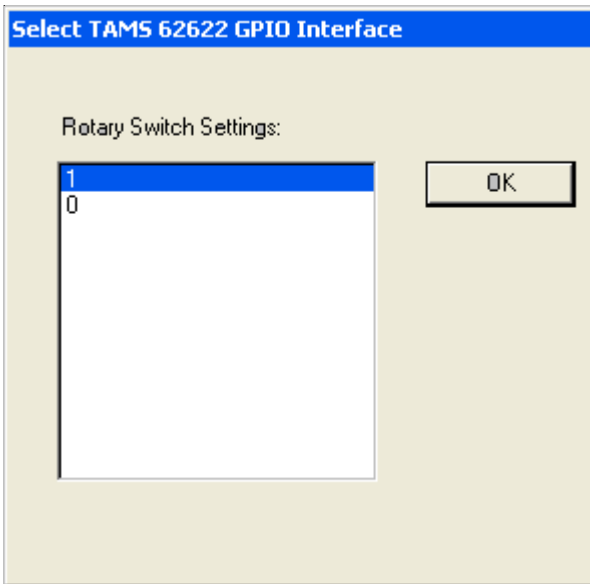
2. Insert the TAMS 62622 CD that was shipped with your 62622 card in the CD drive. The installer should automatically start, if not execute the program tamsva.exe found on this CD.
3. Follow the installation instructions as shown on the screen.

Configuring the Card in Windows 2000 / XP

1. Make sure that the TAMS 62622 card(s) and the t62622 driver are installed and the computer has rebooted. This will cause a default configuration to be setup.
2. Configure the interface by executing the tamsgpio.exe program which may be found by the shortcut under the Start menu: Programs -> TAMS GPIO -> TAMS GPIO Utility.

Upon execution, the tamsgpio program will present one of two windows depending on if one or multiple 62622 cards are installed in your system.

The following window occurs if two or more 62622 cards are installed on your PC. It allows one to select an interface, based on the settings of the rotary switches on the cards, which tamsgpio will communicate with.



After clicking OK, or if only one 62622 interface is configured, tamsgpio will bring up the main screen. To change the configuration of your 62622 card click on the menu item:

Configure -> Interface.

The following screen will be displayed.

TAMS 62622 Configuration

Interface Name: Data Fort:

Logical Unit: Handshake:

Need Clock:

Electrical to Logical Polarity

- DOUT: HI = 1
- DIN: HI = 1
- PSTS: HI = Not CK
- PFLE: HI = Ready
- PCTL: HI = Set

Clear DOUT at Reset

PCTL Delay: ns
Must be between 50 and 61410 ns

PCTL, DOUT, CTL0, CTL1, I/O, and PRESET Pul-up Resistors ON

Modify any parameters you want to change and then click on OK.

Configuration Information

The appendixes provides a detailed, functional description of the TAMS 62622 GPIO interface. You will need to understand this information in order to set the appropriate configuration values for the TAMS 62622. This information should also be helpful when you are preparing and installing the GPIO cable.

After you have read the appendixes and decided how you want to configure the TAMS 62622, refer to your software documentation to configure your I/O application software for the TAMS 62622. (See the “Related Software Documentation” section of this guide.)

The appendixes contains the following information:

- Connector Pinouts
- Data Lines
- Peripheral Information Lines:
 - ◆ Peripheral Control and Peripheral Flag Handshake Lines (PCTL and PFLG)
 - ◆ Input/Output Direction Control Line (I/O)
 - ◆ Peripheral Status Line (PSTS)
 - ◆ Peripheral Reset Line (P_RESET)
 - ◆ External Interrupt Request Line (EIR)
 - ◆ Control Output Lines (CTL0 and CTL1)
 - ◆ Status Input Lines (STI0 and STI1)

Simple IO via the tamsgpio Utility

This program has already been mentioned previously since it allows one to configure the TAMS 62622 card. However, it also has the capability to perform reads and writes to the TAMS 62622 card and any GPIO bus connected thereto.

The tamsgpio program can be run by clicking under the Start menu at: Programs -> TAMS GPIO -> TAMS GPIO Utility

Since a GPIO system often has a custom configuration of the software and hardware connected to the bus, this program is very handy to test out a given configuration. As described previously, the 62622 card and driver can be configured via this utility and then simple reads and writes to your GPIO bus/device can be done from this utility. If the GPIO system installation and configuration is correct then the reads and writes will complete normally. Typically, GPIO must have devices (one is the 62622) on both ends of the bus (i.e. cable) for a read or write to complete on the bus. However, the ASYNC handshake mode can be used if no device is connected to the GPIO bus besides the 62622.

For more details about tamsgpio launch the manual by clicking under the Start menu at: Programs -> TAMS GPIO -> Documentation -> Tamsgpio Manual.

Alternate Configuration Method

An alternative way to configure the TAMS 62622 card is using the Agilent program iocfg32.exe. Under normal circumstances this method would not be used. However, if for some reason the 62622 driver, upon installation, does NOT setup a default configuration then this method can be used.

1. Configure the interface by executing the IO Config program, which will be located in the SICL/bin subdirectory.

Note: Alternate Configuration of TAMS 62622 interface assumes knowledge of SICL configuration procedures specific to the platform. Refer to SICL documentation.

If the Agilent IO Libraries were installed using the default values the SICL\bin subdirectory will be found here:

I/O Libraries Suite 14: C:\Program Files\Agilent\IO Libraries Suite\bin\
M.01.01: C:\Program Files\Agilent\IO Libraries\bin\

2. In the “Available Interface Types” list box select “TAMS 62622 PCI-GPIO” and click the Configure button.
3. If more than one 62622 card is present on the system, select the card to configure. (If more than one card is present a select interfaces window will appear. if you have only

one card continue to step 6.)

4. Select the 62622 card you wish to configure (if you have one card only, you will not need to select any card, the system will do it automatically for you).
5. Click on OK.
6. Now a window will appear that shows all the 62622 configuration parameters.
Modify or accept default configuration parameters. For context sensitive help click on the Help button or press the F1 key.
7. When you are finished setting the configurable parameter, dismiss the dialog box by clicking the OK button.

Appendix A: General Specifications

PCI implementation: revision 2.3, 32 bit, 33 Mhz

Card voltage and current requirements (PCI)

+5 volts, +-5%, at 450 ma max (plus current sourced from pin 50)

(changing pullup resistors may increase max current draw on +5)

+3.3 volts, +- 10%, at 1 ma max

PCI signaling voltage:

3.3 or 5 volts, universal keying

Dimensions:

4.75 by 4.00 inches, exclusive of PCI bracket

Weight:

4.0 oz.

Operating environment:

0 to 40 degrees C

10 to 90% RH, non-condensing

Storage environment:

-20 to 70 degrees C

5 to 95% RH

Maximum Current Sourced by Card:

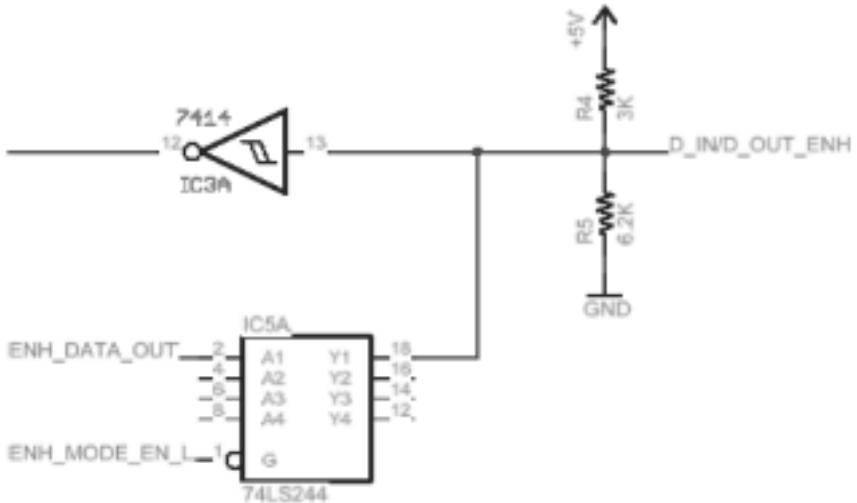
.75 amps from pin 50 of I/O connector.

Appendix B: Input Circuitry

There are two types of inputs to the card, the DIN lines, and the control lines. The DIN lines are input-only in compatibility mode, and bi-directional in enhanced mode. The input control lines are always input-only. The diagram below shows typical circuitry for the DIN lines, the control lines are similar but do not include the 74LS244 type of driver.

As inputs, the lines are biased at approximately 3.3 volts to provide a high level. Schmitt trigger logic is used to help reduce problems from slow transitions, or noisy environments. When these lines are driven as outputs (only during enhanced mode), they can source 15 ma and sink 24 ma. Since these are implemented with TTL logic, the input voltage range is 0-5 volts.

If these inputs are receiving signals from a long cable, note that the high impedance of the circuitry provides no significant termination of the lines. It may be necessary to provide termination between the cable and the 62622 card in order to achieve good signal integrity. See the GPIO support section of the TAMS website for additional information on using long cables.



Typical input line/enhanced line circuitry (DIN_x).

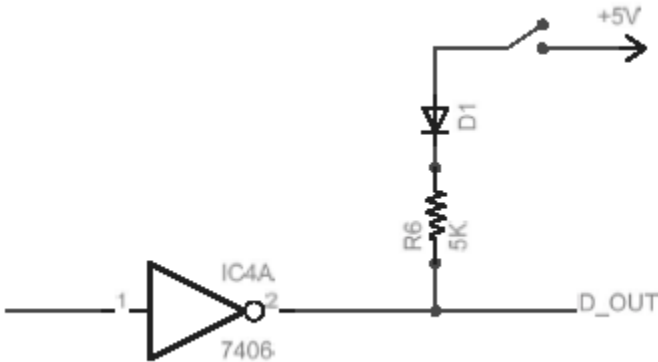
Input control lines similar, but without the 74LS244.
(PFLG, PSTS, ST_0, ST_1, EIR)

Appendix C: Output Circuitry

A typical output line from the card is shown below. The driver is open-collector TTL, with 40 ma pulldown capability and 30 volt max voltage rating. As a convenience, there are 5K pullup resistors to +5 volts provided. These can be switched on and off under software control. They are also socketed, to allow easy reconfiguration if the external circuitry requires. If they are re-inserted, be careful to observe the polarity markings.

The blocking diodes are rated at greater than 30 volts, so the on-board resistors can safely be left installed if the outputs are pulled up to greater than 5 volts by external circuitry. Care must be taken to insure that the voltage on the outputs does not exceed 30 volts due to transmission line effects from a cable, or inductive kickback from switched loads.

The pullup resistors are 12 pin SIP style, installed in J1-J4. If driving long cables, signal quality constraints may require lower value resistors to provide adequate current sourcing. Never use less than 120 ohms, due to current constraints of the drivers. One suitable source for 120 ohm resistor packs is Bourns, PN 4612X-102-121LF, four per card. These are 'isolated' resistors, not 'bussed' or 'common'. See the GPIO Support section of the TAMS website for more information on using long cables.

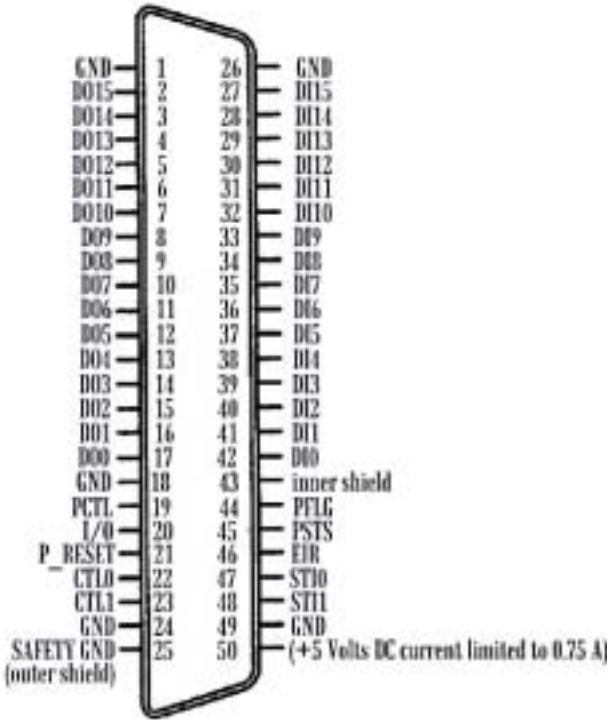


Example circuitry for output lines.

(DOUT_x, PCTL, CTL_0, CTL_1, I/O, P_RESET)

Appendix D: Connector Pinouts

The following figure shows you the Pinouts on the TAMS 62622 GPIO interface connector.



TAMS 62622 GPIO Connector Pinouts

Appendix E: Wiring

Data Lines

There are 32 data lines on the TAMS 62622: 16 designated as data input, and 16 designated as data output. Color codes are provided for TAMS 622-001 GPIO cable and HP 5061-4209 GPIO cable.

Label	Line(s)
DIO through DI15	Data Input
DO0 through DO15	Data Output
PCTL and PFLG	Peripheral Control and Peripheral Flag handshake
I/O	Input/Output direction control
PSTS	Peripheral Status
P_RESET	Peripheral Reset
EIR	External Interrupt Request
CTL0 and CTL1	Control Output
STI0 and STI1	Status Input
PIN 50	+5 Volts DC current limited to 0.75 A

Data Input Lines

The 16 data input lines are labeled DIO through DI15. The following table lists the connector pin numbers and cable wire color codes for the data input lines.

Data Input Lines

Label	Pin No.	622-001	5061-4209
DIO	42	White on Grey	Black
DI1	41	Brown on Blue	Brown
DI2	40	White on Violet	Red
DI3	39	White on Blue	Orange
DI4	38	White on Green	Yellow
DI5	37	White on Yellow	Green
DI6	36	White on Orange	Blue
DI7	35	White on Pink	Violet
DI8	34	Brown on Green	White/Brown/Red
DI9	33	Brown on Yellow	White/Brown/Orange
DI10	32	Brown on Orange	White/Brown/Yellow
DI11	31	Brown on Pink	White/Brown/Green
DI12	30	Tan on Grey	White/Red/Orange
DI13	29	Tan on Violet	White/Red/Yellow
DI14	28	Tan on Blue	White/Red/Green
DI15	27	Tan on Green	White/Red/Blue

Data Output Lines

The 16 data output lines are labeled DO0 through DO15. The following table lists the connector pin numbers and wire color codes for the data output lines.

Data Output Lines

Label	Pin No.	622-001	5061-4209
DO0	17	Grey on White	White/Black
DO1	16	Blue on Brown	White/Brown
DO2	15	Violet on White	White/Red
DO3	14	Blue on White	White/Orange
DO4	13	Green on White	White/Yellow
DO5	12	Yellow on White	White/Green
DO6	11	Orange on White	White/Blue
DO7	10	Pink on White	White/Violet
DO8	9	Green on Brown	White/Orange/Yellow
DO9	8	Yellow on Brown	White/Orange/Green
DO10	7	Orange on Brown	White/Orange/Blue
DO11	6	Pink on Brown	White/Orange/Violet
DO12	5	Grey on Tan	White/Yellow/Green
DO13	4	Violet on Tan	White/Yellow/Blue
DO14	3	Blue on Tan	White/Yellow/Violet
DO15	2	Green on Tan	White/Yellow/Gray

Peripheral Information Lines

The following table lists the connector pin numbers and wire color codes for the peripheral information.

Peripheral Information Lines

Label	Pin No.	622-001	5061-4209
GRD	1	Yellow on Tan	
GRD	18	Violet on Brown	
PCTL	19	Tan on White	White/Grey
I/O	20	Grey on Brown	White/Black/Brown
P_RESET	21	Orange on Pink	White/Black/Red
CTL0	22	Brown on Tan	White/Red/Violet
CTL1	23	Pink on Tan	White/Red/Grey
GRD	24	Brown on White	
Safety GRD	25	Orange on Tan	
GRD	26	Tan on Yellow	
Safety GRD	42	Brown on Violet	
PFLG	44	White on Tan	Grey
PSTS	45	Brown on Grey	White/Black/Grey
EIR	46	Pink on Orange	White/Brown/Grey
STI0	47	Tan on Brown	White/Brown/Blue
STI1	48	Tan on Pink	White/Brown/Violet
GRD	49	White on Brown	
+5 (fused)	50	Tan on Orange	

Appendix F: Differences between the TAMS 62622 & HP2074

TAMS 62622 PCTL delay.

The TAMS 62622 is much more flexible than earlier GPIO interfaces. With the HP 2074/5 interface a user has a choice of one of eight delay time values. The gaps between those values are significant and prevent performance optimization. The TAMS 62622 (and 61622) accepts values directly in nanoseconds. The range extends from 30 nanoseconds to 61410 nanoseconds (61.41 microseconds) allowing optimal transfer performance according to the length of cable used. In addition, the time specified is applied with accuracy at any mode of transfer, while other available GPIO interfaces add as much as 500 ns when different modes are used. This caused users to configure more PCTL time than actually required in order to compensate for worst possible transfer mode cases.

The TAMS 62622 PCTL delay is configurable by running the tamsgpio.exe utility (Windows), or it may be changed at any time from within an application by a call to `igpioctrl(id, I_GPIO_PCTL_DELAY, <val>)` where `<val>` could be one of eight HP 2074/5 compatible values, or directly a value in nanoseconds between 30 and 61410 inclusive.

The eight (0-7) of HP 2074/5 compatible values are interpreted as follows:

0 - 200ns, 1 - 400ns, 2 - 700ns, 3 - 1.2us, 4 - 2us, 5 - 5us, 6 - 10us, 7 - 50us.

The PCTL delay is the delay between data being driven out on the DOUT lines, and PCTL being asserted to the 'set' state to begin the handshake.

Booting after configuring is not required

When TAMS 62622 PCI GPIO card is configured via the tamsgpio.exe interface configuration it does not require rebooting to take effect.; it immediately takes effect for the tamsgpio utility.

Normally, changes take effect when a session on a newly configured interface is started for the first time within a process.

Care should be taken when a running application uses the TAMS 62622 interface while it is being configured.

If, for example, a polarity is changed on an interface while an application uses it, it may take effect in the middle of a transfer causing undesired effects. However, no fatal effects like system crash or device hang could be caused.

Note: On Windows, rebooting is required only once after t62622 driver installation.

Appendix G: TAMS 62622 SICL Extensions

All of the TAMS 62622 SICL extension functions are implemented by using the header file `tamsgpio.h` and the standard SICL functions `igpioctrl` and `igpiostat`.

These two functions are described below:

IGPIOCTRL

Supported sessions: **interface**
Affected by functions: **ilock, itimeout**

C Syntax

```
#include <sicl.h>
#include <tamsgpio.h>
```

```
int igpioctrl (id, request, setting);
```

```
INST id;
int request;
unsigned long setting;
```

Visual Basic Syntax

```
Function igpioctrl
(ByVal id As Integer, ByVal request As Integer,
ByVal setting As Long)
```

IGPIOSTAT

Supported sessions: **interface**

C Syntax

```
#include <sicl.h>
#include <tamsgpio.h>
```

```
int igpiostat (id, request, &result);
```

```
INST id;
int request;
unsigned long result;
```

Visual Basic Syntax

```
Function igpiostat  
(ByVal id As Integer, ByVal request As Integer, ByVal  
result as Long)
```

The following are all the TAMS 62622 SICL extensions and some examples of how to use them.

TAMS 62622 PCTL delay

The TAMS 62622 PCTL delay function provides an extended PCTL delay control that was not available before the 61622. Besides the standard 0-7 values for delay used with HP cards, the delay of the TAMS 62622 may be set directly in nanoseconds.

Examples:

```
Setting the PCTL delay to 50000ns  
    igpioctrl(id, I_GPIO_PCTL_DELAY, 7)  
    or  
    igpioctrl(id, T_GPIO_DLY_TM, 50000)
```

```
Reading the PCTL delay in nanoseconds  
    igpiostat(id, T_GPIO_DLY_TM, &dtm)
```

Legal values are 30 to 61410 ns. The value configured in 'iocfg32.exe' is used, unless overwritten by an igpioctrl call.

TAMS 62622 Filtering

This feature controls the filtering time for DIN[0..15], PCTL, STI0, STI1 and EIR lines for glitch rejection. Filtering is always enabled.

T_GPIO_FLT_TM sets the time in nanoseconds. No individual lines can be controlled. Since the PCTL handshake line is filtered to the same extent as the data lines, the setup time during a read does not change when filtering is increased.

Examples:

Using `T_GPIO_FLT_TM`

Set filtering to 1 microsecond

```
igpioctrl(id, T_GPIO_FLT_TM, 1000)
```

Read the time set for filtering in nanoseconds

```
igpiostat(id, T_GPIO_FLT_TM, &ftm)
```

Legal values are 0 to 1860 ns, where 0 ns provides no filtering. This is the default.

TAMS 62622 Polarity

This function allows the setting of detailed polarity for DIN, STI0, STI1, and EIR. The polarity of nineteen lines (DIN[0..15], STI[0..1] and EIR) can be controlled independently. It enables interrupt control of either rising or falling edge of a line level transition.

For DIN lines interpretation and implementation of the polarity is identical to the standard GPIO DIN polarity as described in the SICL manuals.

For the STI and EIR lines, standard GPIO does not provide polarity configuration. This is a TAMS 62622 extension to the GPIO.

To maintain maximum compatibility with the standard GPIO, the following rules apply:

- ◆ For STI and EIR lines, if polarity is set to 0, their behavior is fully compatible with standard GPIO cards. If set to 1, polarity is reversed for these lines.
- ◆ For DIN lines, 62622 extensions allow to set polarity for each line independently. Setting the DIN polarity with standard GPIO SICL `igpioctrl(id, I_GPIO_POLARITY, setting)` is still supported and will cause setting or clearing of all the DIN polarity lines with a single call. This is because the standard allows only for all the DIN polarity to be set at a time. This guarantees backwards compatibility.

Examples:

Setting polarity for only DIN6 and EIR lines

```
igpioctrl(id, T_GPIO_POL, T_GPIO_DIN06 | T_GPIO_EIR)
```

Reading back the polarity setting

```
igpiostat(id, T_GPIO_POL, &pol);
```

TAMS 62622 Latching

The lines are always latched in the TAMS 62622. The latched lines can be cleared using the `T_GPIO_LAT_CL` request.

- ◆ polarity set to 0, the latch will occur on a LO to HI (logically) transition.
- ◆ polarity set to 1, the latch will occur on a HI to LO (logically) transition.

This function has three different request codes:

- ◆ `T_GPIO_LAT_EN` controls which group of lines are enabled for latching, `DIN[0..15]`, `STI0`, `STI1`, and/or `EIR`.
- ◆ `T_GPIO_LAT_RD` lets you read the latched lines.
- ◆ `T_GPIO_LAT_CL` clears the latched lines.

Examples:

Read latched lines

```
igpiostat(id, T_GPIO_LAT_RD, &lat)
```

Clear latched lines

```
igpioctrl(id, T_GPIO_LAT_CL, lat)
```

TAMS 62622 line interrupt

This function controls which lines can cause an interrupt. Interrupts on level transitions on `DIN[0..15]`, `STI[0..1]` and `EIR` lines can be controlled individually. The interrupt occurs on LO to HI (logically) transition on a GPIO line assuming polarity set to 0, or HI to LO transition if polarity is set to 1 (please refer to point 3: TAMS 62622 Polarity).

If this function is combined with the TAMS 62622 latching function, then the interrupt will fire only once. If this is the case, the latched lines need to be cleared before other interrupts could happen from those lines.

If one or more interrupts occur, the interrupt handler is called with the “reason” parameter equal to `I_INTR_TAMS_GPIO` and the “sec” parameter is the mask of the lines that interrupted.

The mask is always a subset of the following:

`T_GPIO_DIN|T_GPIO_STI|T_GPIO_EIR|T_GPIO_RDY`

Examples:

Enabling DIN4 line to interrupt

```
igpioctrl(id, T_GPIO_INT_EN, T_GPIO_DIN04)
```

Checking what lines can interrupt

```
igpiostat(id, T_GPIO_INT_EN, &int)
```

TAMS 62622 Pull-up

T_GPIO_PUL controls if DOUT[0..15], CTL[0..1], PCTL, I/O, and PRESET lines are pulled up with 5K resistors to +5V. There is no control for individual lines. All lines are controlled at the same time. The SIP packs can be removed or changed by users.

Examples:

Turn all pull-ups ON

```
igpioctrl(id, T_GPIO_PUL, 1)
```

Check if pull-ups are on

```
igpiostat(id, T_GPIO_PUL, &pull)
```

Turn pull-ups OFF

```
igpioctrl(id, T_GPIO_PUL, 0)
```

TAMS 62622 Board ID

The 62622 is equipped with a rotary switch. This switch is set by the user. The purpose is identification of each GPIO board on a system with multiple GPIO cards. This feature is especially useful if multiple boards are residing on a PCI bridge since some PCI bridges do not provide a reliable way for PCI slot identification.

This switch has no effect on the GPIO functionality itself.

The user would set the switch position to a unique value for each card on the system and then can read its value by using the request **T_GPIO_CID** to check which physical card has an opened session.

Examples:

Get the switch value

```
igpiostat(id, T_GPIO_CID, &swID)
```

TAMS 62622 Macros

The following is a table with all of the defines passed to the TAMS 62622 SICL extension functions that you can use. Individual bits could be passed to select specific line(s).

TAMS 62622 SICL extension function defines

T_GPIO_DIN	0x0000FFFF	DIN[0..15] 16 bits
T_GPIO_DIN_L	0x000000FF	DIN[0..7] 8 bits
T_GPIO_DIN_U	0x0000FF00	DIN[8..15] 8 bits
T_GPIO_STI	0x00030000	STI[0..1] 2 bits
T_GPIO_EIR	0x00040000	EIR 1 bit
T_GPIO_RDY	0x00080000	RDY 1 bit
T_GPIO_MORE	0x00100000	used inside driver only
T_GPIO_ALL	0x000FFFFFF	all of above 20 bits

Individual bits for DIN and STI

T_GPIO_DIN00	0x00000001
T_GPIO_DIN01	0x00000002
T_GPIO_DIN02	0x00000004
T_GPIO_DIN03	0x00000008
T_GPIO_DIN04	0x00000010
T_GPIO_DIN05	0x00000020
T_GPIO_DIN06	0x00000040
T_GPIO_DIN07	0x00000080
T_GPIO_DIN08	0x00000100
T_GPIO_DIN09	0x00000200
T_GPIO_DIN10	0x00000400
T_GPIO_DIN11	0x00000800
T_GPIO_DIN12	0x00001000
T_GPIO_DIN13	0x00002000
T_GPIO_DIN14	0x00004000
T_GPIO_DIN15	0x00008000
T_GPIO_STI0	0x00010000
T_GPIO_STI1	0x00020000

Warranty Information

ONE YEAR LIMITED WARRANTY

Test & Measurement Systems, Inc. warrants to the purchaser that the Interface card will be free of all defects in material and/or workmanship for one year from the date of shipment to the customer.

In the event of malfunction or failure attributable directly to faulty material and/or workmanship, TAMS will at its option, repair or replace the defective product or components, to whatever extent it shall deem necessary to restore the product or component, to proper operating condition. TAMS may at its option repair or replace, a defective unit with a new or refurbished unit.

The customer shall be solely responsible for the failure of any TAMS product, resulting from accident abuse, or misapplication of the product, and TAMS assumes no liability as a consequence of such events under the terms of this warranty.

While TAMS has made every effort to provide clear and accurate technical information about the application of this product, TAMS assumes no liability for any events arising out of the use of this technical information.

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TAMS 62622 GPIO Interface

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